

Docket No.: 57810-020

PATENT

2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yasunori INOUE, et al.

Serial No.: 09/810,420

Filed: March 19, 2001

For: SEMICONDUCTOR DEVICE



Group Art Unit: 2815

Examiner: Sheila V. Clark

THE COMMISSIONER FOR PATENTS AND TRADEMARKS
Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

☐
☐
☐

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	19	22	0	\$18.00 =	\$0.00
Independent Claims	6	4	2	\$84.00 =	\$168.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$168.00
Total of Above Calculations					\$0.00
					\$168.00

☒ Please charge my Deposit Account No. 500417 in the amount of \$168.00. An additional copy of this transmittal sheet is submitted herewith.

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: January 7, 2003

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1C 2800 MAIL ROOM

Docket No.: 57810-020



#7/Amendment A
1-14-03
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AMENDMENT

Commissioner for Patents
Washington, DC 20231

Sir:

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TC 2800 MAIL ROOM

The following remarks and amendments are submitted in response to the non-Final Office Action dated October 7, 2002.

IN THE CLAIMS:

Please amend claims 1, 4, 9, 10 and 11 as shown. Please cancel claims 5, 7, and 8 without prejudice or disclaimer of their subject matter.

1. (Amended) A semiconductor device comprising:

a first semiconductor chip consisting of at least either a circuit against static damage or a passive component;

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